



Intelligent to the Core.™

Netronome to Present Keynote Address at ANCS 2008

CTO Derek McAuley to discuss “Exploiting Heterogeneous Cores for Network Flow Processing” at prestigious research conference

PITTSBURGH, PA – November 4, 2008 – Netronome Systems, Inc., a leading developer of highly programmable semiconductor products that provide intelligent and secure flow processing in network and communications devices, today announced that Derek McAuley, the company’s Chief Technology Officer, will present a keynote address at the 4th Annual ACM/IEEE Symposium on Architectures for Networking and Communications Systems (ANCS), taking place November 6 and 7, 2008 at the Hayes Mansion in San Jose, California. The keynote, entitled, *Exploiting Heterogeneous Cores for Network Flow Processing*, will take place on November 7 from 8:30-9:30 a.m.

McAuley is an industry expert with over 20 years of R&D experience in computing, networking and communications systems; a current fellow of the British Computer Society (BCS) and affiliated lecturer at the University of Cambridge Computer Laboratory. He has extensive experience with virtualization, operating systems, networking and systems architectures, and will focus his presentation on how network flow processing requirements can stretch from straightforward Layer 2-4 forwarding to application-specific Layer 4-7 processing, depending on the service location in the network and the service being supported. He will also use recent research examples to explore which processor architectures are better suited for different components of this processing with regard to power consumption, silicon density, predictability, flexibility and cost.

“Because processor architectures vary, engineers need to have a solid understanding of which approaches are best suited for different layers of processing and how to optimize power consumption, as well as ensure flexibility and a host of other capabilities,” said McAuley. “My ANCS presentation is based on extensive research in this area, and will provide attendees with actionable information about the features and capabilities of different chips.”

ANCS is a research conference that focuses on the architecture and design of hardware and software for modern communications networks. This year, the conference will concentrate on hardware and software elements in the context of systems that enable networks to evolve and scale. For more information about ANCS, visit www.cse.wustl.edu/ANCS/.

About Netronome Systems

Netronome is a leading supplier of highly programmable semiconductor products that are used for intelligent flow processing in network and communications devices. Netronome’s solutions include tightly integrated network flow processors and acceleration cards that scale to more than 20 Gbps. They are used in carrier-grade and enterprise-class communications products that require deep packet inspection, flow analysis and content processing. Netronome is headquartered in Pittsburgh, PA, with core operations in San Jose, CA and Boxborough, MA, and international operations in the United Kingdom, China and South Africa. To learn more about Netronome and its products, please visit www.netronome.com.

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