



Intelligent to the Core.™

Netronome Offers Reference Design for 40 Gbps Deep Packet Inspection in Cyber Security Applications

*Leading supplier of network flow processors to demonstrate solution at ISS World Americas
2010*

Washington, DC – October 12, 2010 – Netronome, the leading developer of network flow processors used for cyber security and intelligent networking applications, today announced the availability of a production-ready reference design that delivers 40 Gbps of deep packet inspection. The turnkey, hardware and software solution is purpose-built for cyber security applications that require granular content inspection for the stateful processing of millions of simultaneous flows.

Netronome will demonstrate the design at the Intelligence Support Systems (ISS) World Americas taking place October 11 – 13, 2010 at the Bethesda North Marriott Hotel & Conference Center in Bethesda, Maryland. ISS World Americas is the world's largest gathering of law enforcement, intelligence, homeland security analysts and telecom operators responsible for lawful interception, electronic investigation and network intelligence. In addition, Netronome will also be exhibiting at the event in booth 203.

Netronome's solution is powered by the industry's first family of network flow processors, the NFP-3240. With 40 network- and security-optimized cores operating at 1.4 GHz, the NFP-3240 brings breakthrough performance to a broad range of demanding network, security and content processing products used in high performance cyber security applications. Netronome's processors are supported by comprehensive tools, and a broad ecosystem of premier partners and suppliers.

In addition to exhibiting, Robert Truesdell, a senior engineer at the company, will present "Evolving x86-Based Architectures for 40 Gbps Deep Packet Inspection Designs." His session will be held on Wednesday from 12 – 12:30 p.m. during Track 4: DPI and Intelligent Probes for Lawful Interception, Network Security and Traffic Management.

"With the increase in bandwidth of today's networks, current processing architectures are struggling to meet the demand of increasing traffic rates while also maintaining network intelligence," said Mr. Truesdell. "To overcome these challenges creative techniques to leverage x86 or regular expression processors are necessary to scale designs to 40 Gbps. My presentation will describe an architecture that enables network co-processing to take responsibility for network I/O, scalable flow processing at line-rate, and intelligent flow dispatching to DPI engines."

Information about ISS World Americas can be found at http://www.issworldtraining.com/ISS_WASH/.

About Netronome

Netronome is a leading developer of highly programmable semiconductor products that are used for intelligent flow processing in network and communications devices. Netronome's solutions include network flow processors and acceleration cards that scale from 10 to 100 Gbps. They are used in carrier-

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grade and enterprise-class communications products that require deep packet inspection, flow analysis, content processing, virtualization and security. Netronome's products are developed in labs in Santa Clara, CA, Boxborough, MA and Pittsburgh, PA. To learn more about Netronome and its products, please visit www.netronome.com.

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