



*Intelligent to the Core.™*

## **Netronome Introduces Software Development Tools for Network Flow Processors™**

*Netronome Programmer Studio™ delivers comprehensive, intuitive and familiar software suite, that includes backward-compatibility with Intel® IXP28XX designs*

**PITTSBURGH, PA – March 31, 2009** – Netronome Systems, a leading developer of highly programmable semiconductor products that provide intelligent and secure flow processing for network and communications equipment, today announced the availability of the Netronome Programmer Studio, a comprehensive suite of intuitive and familiar software applications and tools for Netronome's programmable Network Flow Processors and acceleration cards.

The Programmer Studio provides a graphical development environment and supports high-level programming languages for ease-of-use and rapid feature development. The development kit provides leading tools to support all phases of the project, including initial design, code creation, simulation, testing, debugging and optimization. The tool suite allows software and hardware teams to simultaneously develop next-generation communications platforms that take full advantage of the capabilities available in highly-programmable network flow processors.

Derived from the Intel IXA software development kit (SDK), the Programmer Studio improves upon the extensive investment in the widely used tools for the most popular network processor architecture. New users taking advantage of the enhanced capabilities of Netronome's Network Flow Processors (NFP) and Network Flow Engine (NFE) acceleration cards will benefit from a proven development environment, while developers already implementing IXP28XX designs will benefit from the common user interface and source-code compatibility between the IXP28XX and new products from Netronome.

The Netronome Programmer Studio offers a number of key benefits:

- **High-Level Programming Language:** The Network Flow C Compiler provides a familiar high-level programming language with isolation from hardware specifics, for faster time-to-market and optimum code portability. Assembly code can also be embedded in the Network Flow C Compiler for optimal results utilizing both languages.
- **Investment Protection and Extension:** Full backward-compatibility with existing IXP28XX source-code, as well as enhanced support for new features of Netronome Network Flow Processors and acceleration cards.
- **Choice of Development Platforms:** Support includes the Netronome's Network Flow Processor customer reference platforms and multi-gigabit PCIe Network Flow Engine acceleration cards.
- **Extensive Software Libraries:** Support for a wide range of software and popular L2-L7 protocols, including the production-ready Netronome Flow Management software, reference code, drivers, and existing IXP28XX microblock software.
- **Advanced Flow Processing Development Tools:** The Precision Flow Modeler provides cycle- and data-accurate simulation for rapid prototyping and optimization of applications. Additional traffic simulation and packet generation tools further reduce testing and debugging time.
- **Industry-wide Support:** The Netronome SDK enters the market backed by an extensive pool of associated research papers, sample and production quality software, documentation, books and other published materials created in support of the Intel IXA SDK.

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“Rapid feature development and investment protection are increasingly important to organizations,” said Jim Finnegan, senior vice president of engineering at Netronome. “The Netronome Programmer Studio helps drive new products to market quickly through IXP28XX code compatibility with tool familiarity. Allowing developers to apply their preexisting knowledge to Netronome’s new products preserves their prior code and talent investments and greatly reduces cost, risk and time-to-market for next generation communications systems.”

The Programmer Studio development environment is currently available to customers of Netronome’s Network Flow Processors and Network Flow Engine acceleration cards, as well as existing Intel IXP28XX users. Netronome is demonstrating the full product suite at the Embedded Systems Conference in booth #444, March 31-April 2, 2009, at the McEnery Convention Center in San Jose, California.

#### **About Netronome Network Flow Processors™**

To avoid costly, lengthy, and risky custom ASIC developments, many companies have further embraced merchant silicon providers of highly programmable network processors. For designers of communications equipment whose network processing requirements extend beyond simple forwarding, Netronome’s Network Flow Processors deliver high-performance packet processing with intelligence, security and virtualization. They are powered by 40 programmable networking cores that deliver over 1500 instructions and 50 flow operations per packet at 30 million packets per second, enabling 20Gbps of L2-L7 processing with line-rate security and I/O virtualization for millions of simultaneous flows. In addition, Netronome’s Network Flow Processor technology is backward-compatible with the market-leading Intel IXP28XX network processor, protecting customers’ substantial investments in field-proven and network-hardened software.

#### **About Netronome Systems**

Netronome is a leading developer of highly programmable semiconductor products that are used for intelligent flow processing in network and communications equipment. Netronome’s solutions include network flow processors and acceleration cards that scale to more than 20Gbps. They are used in carrier-grade and enterprise-class communications products that require deep packet inspection, flow analysis, content processing, virtualization and security. Netronome is headquartered in Pittsburgh, PA, with core operations in San Jose, CA and Boxborough, MA, and international locations in the United Kingdom, China and South Africa. To learn more about Netronome and its products, please visit [www.netronome.com](http://www.netronome.com).

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